

(Formerly Nagole Institute of Technology & Science) Abdullapurmet(M), Near Hayathanagar, Hyderabad.

Ref No: PEC/ECE/Nov-2020

DATE: .25.11.2020

#### DEPARTMENT OF ECE CIRCULAR

All the Staff and students are here by informed that the department of ECE is going to conduct a "Two Week Certificate Program On FPGA Programming" for Third and final year ECE students for two weeks i.e., from 07.12.2020 to 19.12.2020 from 10.00am to 4.00pm.

All students should consider it as a mandatory and attend the program.

To

All Heads of the Departments, O/O Principal, PEC

PALLAVI ENGINEERING COLLEGE
(Formerly Nagole Institute of Technology & Science)
Kuntloor (V), Abdullapurmet (M)
1 Jerabad, R.R. Dist-601505, Telangana

PALLAVI ENGINEERING COLLEGE

PALLAVI ENGINEERING COLLEGE

(Formerly Nagole Institute of Technology & science)

Kuntloor (V), Abdullapurmet (M)

Kuntloor (R, Dist-501505, Telangana

jerabad, R.R. Dist-501505



### Two Week Certificate Program on

### **FPGA Programming**

07th to 19th December 2020

### REGISTRATION FORM

1. Name:

2. College:

3. Branch & Yeap ALLAYI ENGINEERING COLLE

4. Category (Stander No.) gole Institute of Technology &

5. Address:

Kuntleor (V), Abdullapurmet (M ferabad, R.R. Dist-501505, Telang

6. Mobile No.:

7. E-mail:

Place :

Date :

Signature

#### **ABOUT DEPARTMENT**

#### Vision

The vision of the ECE Department is to become a nationally and internationally leading department of higher learning, building upon the culture and the values of universal science and contemporary education and a center of research and education generating the knowledge and the technologies which lay the groundwork in shaping the future in the fields of Electronics and Communication engineering

#### Mission

The mission of the ECE Department is to provide education for those students who are able to compete internationally, able to produce creative solutions to the society's needs, conscious to the universal moral values. adherent to the professional ethical code and to generate and disseminate knowledge and technologies essential to the local and global needs in the field of electronics and communication engineering.

### **About The Program**

The Program on FPGA programming aims at imparting knowledge to students on XLINIX, FPGA. Introducing FPGA technology and VHDL programming language to students enable them to write the programs in interface various devices like LED and LCD, DC motor interface using related commands. This program also includes imparting knowledge about ADC, UART, LCD. How FPGA is related to the different elements, how far is the programming better when compared to other programming languages. Students were able to interface the DC motor using FPGA programming.

#### **CERTIFICATE PROGRAM CONTENTS**

- Introducing FPGA technology and VHDL programming language.
- Interface various devices like LED and LCD. DC motor interface using related commands.
- This program also includes imparting knowledge about ADC, UART,LCD

#### Who can Attend

**UG. PG Students** 

#### **Important Dates**

Early bird Registration: 01-12-2020

Late Registration: 06-12-2020

Confirmation Registration: 06-12-2020

On spot registration will not be entertained as the Number of participants is limited

### **Address for Correspondence**

Mr. B. Laxman, Asst. Prof. Department of ECE,

Pallavi Engineering College, Hyderabad Email: ecehod@pallaviengineeringcollege.ac.in Web: www.pallaviengineeringcollege.ac.in

Mobile:: 9493944033

### **About College**

Pallavi Engineering College has a sprawling campus and is well connected by road and easily accessible by both college and RTC buses. It has the unique advantage of being located in the heart of the city. It is situated 5 km from Nagole cross roads and 3 km from Hayathnagar. It is free from pollution and the hustle and bustle of the city. The experience at PEC is unique; studying here opens a door to a world of opportunities. The college has welldesigned, adequately furnished, ventilated and spacious classrooms. All the laboratories of Engineering and Sciences are well equipped with latest and sophisticated gadgets.

#### Vision

To emerge as a global leader in imparting quality technical education emphasizing ethical values for the betterment of the society.

#### Mission

To create an excellent teaching learning environment and inculcate the aptitude for research.

To establish centers of excellence through collaborative Initiatives.

To empower the student community by developing creativity and innovation

### **Chief Patron**

Sri. Malka Komaraiah, Chairman & Correspondent

### **Patron**

Dr. G. Mahesh Manivanna, Principal

#### Convener

Mr. D. Rupa Kumar, Asst. Prof, ECE

### ORGANISING COMMITTEE

Mr. B. Narendar Reddy, Asst. Prof.
Mr. P. Nanda Kumar,
Asst. Prof. (Hospitality)
Mr. J. Praveen, Asst.Prof (Publicity)

### **RESOURCE PERSON**

Ms .Shalini Bhattacharya,
Kongu Engineering college,
Perunduri
Dr. Senthil Kumar,
Ashoka Institute of
engineering & Technology, Hyd

# Two Week Certificate Program on

### **FPGA Programming**

07<sup>th</sup> to 19<sup>th</sup> December 2020 Organized by

Department of Electronics and Communication Engineering









### Pallavi Engineering College

( Approved by AICTE, New Delhi, Affiliated to JNTUH, Hyderabad )

www.pallaviengineeringcollege.ac.in



(APPROVED BY AICTE, NEW DELHI, AFFILIATED TO JNTUH, HYDERABAD) NAGOLE, HYDERABAD, R.R DIST 501505, TELANGANA STATE, INDIA

#### DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

#### One Week Certificate Program On FPGA Programming

From: 07-12-2020 to 19-12-2020

#### **CURRICULAM OF THE PROGRAM**

- Introduction about FPGA, FPGA Programming & FPGA Architecture
- Introduction to VHDL and UART programming basics
- Hands on training on different modules Programming
- Introduction to XLINIX ISE IDE
- Accessing on-chip ADC and to display the value in LCD

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NAGOLE, HYDERABAD, R.R.DIST 501505, TELANGANA STATE, INDIA

## Two Week Certificate Program On FPGA Programming AGENDA

#### **DAY:01**

7<sup>th</sup> Dec to 19<sup>th</sup> Dec 2020

09:30 am – 10:00 am	Registration
10:00 am – 11:30 am	Opening Session
	- Welcome by host partner.
	- Overview about programming
	<ul> <li>Introduction about chief guest Mr.Balasentilmurugan</li> </ul>
	- Introduction about FPGA
11:30 am – 11:45 am	Coffee Break
11:45 am – 01:15 pm	Session 1.
	- Introduction to FPGA Programming
	- Architecture of FPGA
	- Overview
01:15 pm – 02:15 pm	Lunch
02:15 pm- 03:30 pm	Practice of session 1
03:30 pm – 04:00 pm	Doubts and Discussions

#### **DAY:02**

Session 2
- Introduction to VHDL programming
Commands used in programming
Coffee Break
Session 2 Cont
- Introduction to VHDL programming
- Commands used in programming
Lunch Break
Practice of session 2
Doubts and Discussions

#### **DAY:03**

10:00 am – 11:15 am	Session 3.
	<ul> <li>UART programming basics</li> </ul>
	- overview
11:15 am – 11:30 am	Coffee Break
11:30 am – 01:15 pm	Session 3 Cont
01:15 pm – 02:15 pm	Lunch Break
02:15 pm – 03:30 pm	Practice of session 3
03:30 pm - 04:00 pm	Doubts and Discussions

#### **DAY:04**

DIXIOI		J Kana
10:00 am - 11:15 am	Session 4 Blinking of LED, LEDS	PRINCIPAL PALLAVI ENGINEERING COLLEGE PALLAVI ENGINEERING COLLEGE (Formerly Nagole Institute of Technology & Science (Formerly Nagole Institute of Technology & Science (Formerly Nagole Institute of Technology & Science (Formerly Nagole Institute of Technology & Science)
11:15 am – 11:30 am	Coffee Break	(Formerly Nagole Institute of Testing (M) Kuntloor (V), Abdullapurmet (M) Kuntloor (V), Abdullapurmet (M)  tarabad, R.R. Dist-501505, Telah aria.
11:30 am - 01:15 pm	Session 4 Cont	terabad, R.R. Dist-50 1565
01:15 pm – 02:15 pm	Lunch Break	
02:15 pm – 03:30 pm	Practice of session 4	
03:30 pm - 04:00 pm	Doubts and Discussions	

#### **DAY:05**

Session 5.
- Interface keypad
Coffee Break
Session 5 Cont
Lunch Break
Practice of session 5
Doubts and Discussions

#### **DAY:06**

10:00 am - 11:15 am	Session 6.
	- Hands on training on 7 segment display
	- RTC,EEPROM
11:15 am - 11:30 am	Coffee Break
11:30 am – 01:15 pm	Session 6 Cont
01:15 pm – 02:15 pm	Lunch Break
02:15 pm – 03:30 pm	Practice of session 6
03:30 pm- 04:00 pm	Doubts and Discussions

#### **DAY:07**

10:00 am – 11:15 am	Session 7.  - Hands on training on VHDL Programming.  - Overview
11:15 am – 11:30 am	Coffee Break
11:30 am - 01:15 pm	Session 7 Cont
01:15 pm – 02:15 pm	Lunch Break
02:15 pm – 03:30 pm	Practice of session 7
03:30 pm- 04:00 pm	Doubts and Discussions

### **DAY:08**

10:00 am - 01:15 pm	Session 8.
	- Hands on training on hardware interface
	- On serial port
	- ADC
11:15 am – 11:30 am	Coffee Break
11:30 am – 01:15 pm	Session 8 Cont
01:15 pm – 02:15 pm	Lunch Break
02:15 pm – 03:30 pm	Practice of session 8
03:30 pm- 04:00 pm	Doubts and Discussions

#### DAY:09

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DAY:09		9
10:00 am – 01:15 pm	Session 9.  - DC motor interface - Program with PWM	PRINCIPAL PALLAVI ENGINEERING COLLEGE (Formerly Nagote Institute of Technology & Science) (Formerly Nagote Institute of Technology & Science)
11:15 am - 11:30 am	Coffee Break	(Formerly Nagole Institute of Technology (M)  (Formerly Nagole Institute of Technology (M)  Kuntleor (V), Abdullapurmet (M)  Kuntleor (V), Abdullapurmet (M)  Jerabad, R.R. Dist-501505, Telangana
11:30 am - 01:15 pm	Session 9 Cont	Kumilou R.R. Dist-501503,
01:15 pm – 02:15 pm	Lunch Break	191304
02:15 pm – 03:30 pm	Practice of session 9	
03:30 pm- 04:00 pm	Doubts and Discussions	

#### **DAY:10**

D111.XV	
10:00 am - 01:15 pm	Session 10.
	- Introduction to XLINIX ISE IDE.
	- overview
11:15 am - 11:30 am	Coffee Break
11:30 am - 01:15 pm	Session 10 Cont
01:15 pm – 02:15 pm	Lunch Break
02:15 pm – 03:30 pm	Practice of session 10
03:30 pm- 04:00 pm	Doubts and Discussions

#### **DAY:11**

10:00 am - 01:15 pm	Session 11.
	- Accessing on-chip ADC and to display the value in LCD temperature sensors
11:15 am – 11:30 am	Coffee Break
11:30 am - 01:15 pm	Session 11 Cont
01:15 pm - 02:15 pm	Lunch Break
02:15 pm - 03:30 pm	Practice of session 11
03:30 pm- 04:00 pm	Doubts and Discussions

#### **DAY:12**

10:00 am - 01:15 pm	Session 12.
	- Accessing on-chip ADC to display the value in PC serial terminal temperature sensors.
11:15 am – 11:30 am	Coffee Break
11:30 am – 01:15 pm	Session 12 Cont
01:15 pm – 02:15 pm	Lunch Break
02:15 pm - 03:30 pm	Practice of session 12
03:30 pm- 04:00 pm	Doubts and Discussions
04:00 pm - 04:10 pm	validity and photo session

Co-ordinator

HEAD OF THE DEPARTMENT
Electronics & Communication Engineering
PALLAVI ENGINEERING COLLEGE
Kuntloor (V), Hayathnagar (M)
Ranga Reddy Dist-501 505

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Kuntloor (V), Dist-501505, Telangana.
Perabad, R.R. Dist-501505, Telangana.



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#### **DEPARTMENT OF ECE**

#### SUMMARY REPORT OF THE PROGRAM

S.No.	Name of the Course	Duration	No.of Students Registered	No.of Students Benefited
1	FPGA	12days (60 hours)	66	62

Co-ordinator

HEAD OF THE DEPARTMENT

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Kuntloor (R), Dist-50 1505, Telangana.



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#### DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

#### **OUT COME OF THE PROGRAM**

#### One Week Certificate Program On FPGA Programming

- -Understand the concepts of FPGA Programming and Architecture of FPGA
- -Identify the Application and Purpose of FPGA Programming, UART programming & Design Process Requirements
- Writing of Commands used in programming by using XLINIX ISE IDE tool
- -Hands on training of different modules by VHDL programming on tool of XLINIX ISE IDE

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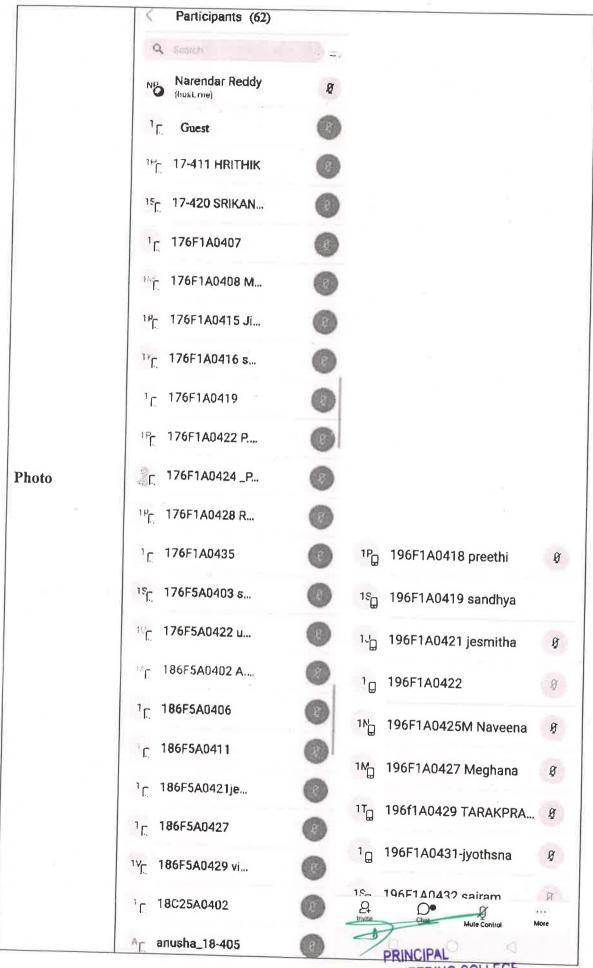


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NAGOLE, HYDERABAD, R.R DIST 50150S, TELANGANA STATE, INDIA

### Activity Report on Two week Certificate Program on FPGA Programming

Name of the Activity	Two week Certificate Program on	FPGA Programming					
Type of Activity	Two week Certificate Program for students						
Date and Time of Activity	07/12/2020 to 19/12/2020 10.00 am to 4.00 pm						
Details of Participants	62 Students participated						
Coordinator(s)	Mr. B Narendar Reddy, Asst. Prof.  Mr. P Nanda Kumar, Asst. Prof.						
Organizing Dept./Support System	ECE						
In collaboration with	Ms. Shalini Bhattacharya, Kongu Engineering college, Perunduri, Dr. Senthil Kumar, Asoc. Prof. Ashoka Institute of Engineering &	Technology, Hyd.					
Description	Inis Program started with an inaguests. After addressing the gather started with introduction to I programming. In the afternoon it was topics discussed in the morning secontents like introduction to XLI with VHDL, LED and LCD interwere trained in hands on session. During this period students were show it is related to the different elebetter when compared to other prograble to interface the DC motor using Valedictory session was held after the started with a started and the started and the started with an inaguest and the started with introduction to I was a started with an inaguest and the started with introduction to I was a started with an inaguest and the started with introduction to I was a started with an inaguest and the started with introduction to I was a started with an inaguest and the started with introduction to I was a	ring by eminent persons, the session FPGA programming and VHDL vas complete hands on session on the ession. This Program included with INIX, interfacing various elements facing. All the interfacing sessions able to understand what FPGA is? ements, how far is the programming gramming languages. Students were					





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1 terabad, R.R. Dist-501505, Telangana

Media coverage (if any)

ND3 Co-ordinator

HEAD OF HEAD PARTMENT
Electronics & Communication Engineering
PALLAVI ENGINEERING COLLEGE
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Kuntloor (V), Hayathnagar (M), R.R. Dist: - 501 505, Greater Hyderabad Two Week Certificate Program On FPGA Programming STUDENTS ATTENDANCE SHEET

#### From 07-12-2020 to 19-12-2020

S.N	Hall ticket no.	Name of the student	Day1	Day2	Day3	Day4	Day5	Day6	Day 7	Day 8	Day 9	Day 10	Day 11	Day 12
1	196F1A0401	A PAVANI	/					X		/	V			V
2	196F1A0403	AMBATI SRUJANA		X						X			1/	V
3	196F1A0404	ANNAM SRIHARI	/			X	1/		/	1		1/	V	
4	196F1A0408	BIGNESH KUMAR	<b>\</b>			1	1		X			V	/	1
5	196F1A0412	CH SOWMYA	V		X	~		1	1	1			1	
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7	196F1A0414	CH PADMA PRIYA	$\vee$			X		/	V	V	V			
8	196F1A0415	D VISHNU VARDHAN	$\sim$	/		V	X			1		1	1	
9	196F1A0420	K V SAI SURESH			/	/	5	A			~	~	1	V
10	196F1A0421	KAVURI JESMITHA	~		X	1	/	1	1	V		1	1	1
11	196F1A0427	P MEGHANA		/	1	-/	1	/	X		./	X	1	
12	196F1A0431	R JYOTHSNA	X	/	1			/	1	1			ž	
13	196F1A0432	SAMPETA SAIRAM	1	/	1	X	1		1	1		7	Ž	×
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17	196F1A0439	VENDRA JYOTHI	1	<b></b>	V	V		V		~	./	~		1
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44	176F1A0401	AKULA SAI TEJA	V	-/	X	~				<b>/</b>		$\checkmark$	$\sqrt{}$	2
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49	176F1A0411	HRITHIK JAISWAL	~		$\sim$		$\sim$			5				
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53	176F1A0416	KOYYALA SATEESH	Ab	Ab	Ab	Ab	Ab	Ab	Ab	.Ab	Ab	Alo	Ab	Ab
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58	186F5A0410	M MAMATHA			V	~	1	~	~			V		
59	186F5A0411	MANGALI SUPRIYA	V	V	1	V	V	/	X	~	1	V	-	V
60	186F5A0415	SESHAGIRI TEMBURU	~	~		V		V	V	V	-	$-\sim$		V
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Co-ordinator

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Allavi Negole Institute of Technology Telangana.

Jerabad, R.R. Dist-501,505, Telangana.

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#### 'FPGA PROGRAMMING'

### Two Week Certificate Program Evaluation Form

Please return this form to the instructor or organizer at the end of the workshop. Thank you.

Program title: FPGA Pro	gramn	ing						
Date: 19-12 - 2020 Instruct	tor:	٠٠٠)	Sen	thil	2	zuma	<u> </u>	
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1. The content was as described in publicity	y materials			1	2	3	4	5
2. This Program was applicable to my job				1	2	3	4	45
3. I will recommend this Program to other of	conservator	`S		1	2	3	4	5
4. The program was well paced within the a	allotted tim	e		1	2	3	4	75
5. The instructor was a good communicator	•			1	2	3	4	5
6. The material was presented in an organiz	zed manner			1	2	3	4	5
7. The instructor was knowledgeable on the	topic			1	2	3	4/	5
8. I would be interested in attending a follow advanced Program on this same subjections.		;		1	2	3	4	15
9. Given the topic, was this Program:	□ a. ′	Too short	· <b>\</b>	b. Rigi	ht ler	igth 🗖	c. To	o long
10. In your opinion, was this Program:	<b>1</b> a. :	Introduct	ory 🗖	b. Inte	erme	diate 🖵 c	. Adv	anced
11. Please rate the following:								
<ul><li>a. Visuals</li><li>b. Acoustics</li><li>c. Meeting space</li><li>d. Handouts</li><li>e. The program overall</li></ul>	Excellent	Very Go	ood	Good		Fair		Poor
12. What did you most appreciate/enjoy/thi improvement?	nk was bes					ggestions	for	
:								,
-					1	1		

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Thank you! PALLAVI ENGINEERING COLLEGE
Please return this form to the instructor or coordinator at the end of the Program. Science)
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#### 'FPGA PROGRAMMING'

#### Two Week Certificate Program Evaluation Form

Please return this form to the instructor or organizer at the end of the workshop. Thank you.

Program title: FPGA Programm	ling				
Date: 19 12 20 Instructor: 16.	SonEhil	Kur	nax		
	Strongly agree				rongly sagree
1. The content was as described in publicity materials	1	2	3	4	5
2. This Program was applicable to my job	1	2	3	4	5
3. I will recommend this Program to other conservators	1	2	3	4	5
4. The program was well paced within the allotted time	1	2	3	4	5
5. The instructor was a good communicator	1	2	3	V	5
6. The material was presented in an organized manner	1	2	3	4	5
7. The instructor was knowledgeable on the topic	1	2	3	4	\$
8. I would be interested in attending a follow-up, more advanced Program on this same subject	1	2	3	4	5
9. Given the topic, was this Program:  a. Too sh	ort 🖵 b. Ri	ght len	igth [	<b>c</b> . Too	long
10. In your opinion, was this Program:	uctory 🖵 b. In	termed	liate 🗖	c. Adva	nced
11. Please rate the following:					
a. Visuals Excellent Very b. Acoustics	Good Good	i	Fair		oor
c. Meeting space					
d. Handouts e. The program overall	3/ O				
12 What did you most appreciate/enjoy/think was best about improvement?			ggestio	ns for	
g					
			_		> _

Thank you!

Please return this form to the instructor or coordinator at the end of the Programmy & Science)

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Rentitoor (V), Abdullapurmet (M)
Noticed R.R. Dist-501505

COORDINATOR

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# CERTIFICATE

OF PARTICIPATION This certificate accredited that

Mr./ Ms./ Mrs. V. Jeevan Kumes \_\_\_\_has successfully

Completed two week Certificate Program on "FPGA Programming" organized by the Department of Electronics and Communication Engineering held at Pallavi Engineering College, Hyderabad.

During 07.12.2020 to 19.12.2020

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