



# PALLAVI ENGINEERING COLLEGE

(Formerly Nagole Institute of Technology & Science)  
Abdullapurmet(M), Near Hayathanagar, Hyderabad.

RefNo: PEC/ECE/Nov- 2020

DATE: .25.11.2020

## DEPARTMENT OF ECE CIRCULAR

All the Staff and students are here by informed that the department of ECE is going to conduct a **“Two Week Certificate Program On FPGA Programming”** for Third and final year ECE students for two weeks i.e , from 07.12.2020 to 19.12.2020 from 10.00am to 4.00pm.

All students should consider it as a mandatory and attend the program.

To

All Heads of the Departments,  
O/O Principal, PEC

  
PRINCIPAL  
PALLAVI ENGINEERING COLLEGE  
(Formerly Nagole Institute of Technology & Science)  
Kuntloor (V), Abdullapurmet (M)  
Hyderabad, R.R. Dist-501505, Telangana

  
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PALLAVI ENGINEERING COLLEGE  
(Formerly Nagole Institute of Technology & Science)  
Kuntloor (V), Abdullapurmet (M)  
Hyderabad, R.R. Dist-501505, Telangana



## Two Week Certificate Program on FPGA Programming

07<sup>th</sup> to 19<sup>th</sup> December 2020

### REGISTRATION FORM

1. Name :

2. College :

3. Branch & Year :

4. Category (Student/Staff)

5. Address :

6. Mobile No. :

7. E-mail :

Place :

Date :

Signature

## ABOUT DEPARTMENT

### Vision

The vision of the ECE Department is to become a nationally and internationally leading department of higher learning, building upon the culture and the values of universal science and contemporary education and a center of research and education generating the knowledge and the technologies which lay the groundwork in shaping the future in the fields of Electronics and Communication engineering

### Mission

The mission of the ECE Department is to provide education for those students who are able to compete internationally, able to produce creative solutions to the society's needs, conscious to the universal moral values, adherent to the professional ethical code and to generate and disseminate knowledge and technologies essential to the local and global needs in the field of electronics and communication engineering.

### About The Program

The Program on FPGA programming aims at imparting knowledge to students on XLINIX, FPGA. Introducing FPGA technology and VHDL programming language to students enable them to write the programs in interface various devices like LED and LCD, DC motor interface using related commands. This program also includes imparting knowledge about ADC, UART,LCD. How FPGA is related to the different elements, how far is the programming better when compared to other programming languages. Students were able to interface the DC motor using FPGA programming.

## CERTIFICATE PROGRAM CONTENTS

- Introducing FPGA technology and VHDL programming language.
- Interface various devices like LED and LCD, DC motor interface using related commands.
- This program also includes imparting knowledge about ADC, UART,LCD

## Who can Attend

UG, PG Students

## Important Dates

Early bird Registration: 01-12-2020

Late Registration: 06-12-2020

Confirmation Registration: 06-12-2020

On spot registration will not be entertained as the Number of participants is limited

## Address for Correspondence

**Mr. B. Laxman, Asst. Prof.**  
**Department of ECE,**

Pallavi Engineering College, Hyderabad

Email: ecehod@pallaviengineeringcollege.ac.in

Web: www.pallaviengineeringcollege.ac.in

Mobile: : 9493944033

## About College

Pallavi Engineering College has a sprawling campus and is well connected by road and easily accessible by both college and RTC buses. It has the unique advantage of being located in the heart of the city. It is situated 5 km from Nagole cross roads and 3 km from Hayathnagar. It is free from pollution and the hustle and bustle of the city. The experience at PEC is unique; studying here opens a door to a world of opportunities. The college has well designed, adequately furnished, ventilated and spacious classrooms. All the laboratories of Engineering and Sciences are well equipped with latest and sophisticated gadgets.

## Vision

To emerge as a global leader in imparting quality technical education emphasizing ethical values for the betterment of the society.

## Mission

To create an excellent teaching learning environment and inculcate the aptitude for research.

To establish centers of excellence through collaborative Initiatives.

To empower the student community by developing creativity and innovation

## Chief Patron

Sri. Malka Komaraiah, Chairman &  
Correspondent

## Patron

Dr. G. Mahesh Manivanna, Principal

## Convener

Mr. D. Rupa Kumar, Asst. Prof, ECE

## ORGANISING COMMITTEE

Mr. B. Narendar Reddy, Asst. Prof.

Mr. P. Nanda Kumar,  
Asst. Prof.(Hospitality)

Mr. J. Praveen, Asst.Prof (Publicity)

## RESOURCE PERSON

Ms .Shalini Bhattacharya,  
Kongu Engineering college,  
Perunduri

Dr. Senthil Kumar,  
Ashoka Institute of  
engineering & Technology, Hyd

Two Week Certificate  
Program on

**FPGA Programming**

07<sup>th</sup> to 19<sup>th</sup> December 2020

Organized by

Department of Electronics and  
Communication Engineering



**Pallavi Engineering College**

( Approved by AICTE, New Delhi, Affiliated to JNTUH,  
Hyderabad )

[www.pallaviengineeringcollege.ac.in](http://www.pallaviengineeringcollege.ac.in)



# PALLAVI ENGINEERING COLLEGE

(APPROVED BY AICTE, NEW DELHI, AFFILIATED TO JNTUH, HYDERABAD)  
NAGOLE, HYDERABAD, R.R DIST 501505, TELANGANA STATE, INDIA

## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

### One Week Certificate Program On FPGA Programming

From: 07-12-2020 to 19-12-2020

#### CURRICULAM OF THE PROGRAM

- Introduction about FPGA, FPGA Programming & FPGA Architecture
- Introduction to VHDL and UART programming basics
- Hands on training on difeerent modules Programming
- Introduction to XLINIX ISE IDE
- Accessing on-chip ADC and to display the value in LCD

HoD

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NAGOLE, HYDERABAD, R.R DIST 501505, TELANGANA STATE, INDIA

## Two Week Certificate Program On FPGA Programming

### AGENDA

**DAY:01**

**7<sup>th</sup> Dec to 19<sup>th</sup> Dec 2020**

09:30 am – 10:00 am	<b>Registration</b>
10:00 am – 11:30 am	<b>Opening Session</b> <ul style="list-style-type: none"><li>- Welcome by host partner.</li><li>- Overview about programming</li><li>- Introduction about chief guest Mr.Balasentilmurugan</li><li>- Introduction about FPGA</li></ul>
11:30 am – 11:45 am	<b>Coffee Break</b>
11:45 am – 01:15 pm	<b>Session 1.</b> <ul style="list-style-type: none"><li>- Introduction to FPGA Programming</li><li>- Architecture of FPGA</li><li>- Overview</li></ul>
01:15 pm – 02:15 pm	<b>Lunch</b>
02:15 pm – 03:30 pm	<b>Practice of session 1</b>
03:30 pm – 04:00 pm	<b>Doubts and Discussions</b>

**DAY:02**

09:30 am – 11:15 am	<b>Session 2</b> <ul style="list-style-type: none"><li>- Introduction to VHDL programming</li><li>Commands used in programming</li></ul>
11:15 am – 11:30 am	<b>Coffee Break</b>
11:30 am – 01:15 pm	<b>Session 2 Cont...</b> <ul style="list-style-type: none"><li>- Introduction to VHDL programming</li><li>- Commands used in programming</li></ul>
01:15 pm – 02:15 pm	<b>Lunch Break</b>
02:15 pm – 03:30 pm	<b>Practice of session 2</b>
03:30 pm – 04:00 pm	<b>Doubts and Discussions</b>

**DAY:03**

10:00 am – 11:15 am	<b>Session 3.</b> <ul style="list-style-type: none"><li>- UART programming basics</li><li>- overview</li></ul>
11:15 am – 11:30 am	<b>Coffee Break</b>
11:30 am – 01:15 pm	<b>Session 3 Cont...</b>
01:15 pm – 02:15 pm	<b>Lunch Break</b>
02:15 pm – 03:30 pm	<b>Practice of session 3</b>
03:30 pm – 04:00 pm	<b>Doubts and Discussions</b>

**DAY:04**

10:00 am – 11:15 am	<b>Session 4.</b> <ul style="list-style-type: none"><li>- Blinking of LED, LEDS</li></ul>
11:15 am – 11:30 am	<b>Coffee Break</b>
11:30 am – 01:15 pm	<b>Session 4 Cont...</b>
01:15 pm – 02:15 pm	<b>Lunch Break</b>
02:15 pm – 03:30 pm	<b>Practice of session 4</b>
03:30 pm – 04:00 pm	<b>Doubts and Discussions</b>

  
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**DAY:05**

10:00 am – 11:15 am	<b>Session 5.</b> - Interface keypad
11:15 am – 11:30 am	<b>Coffee Break</b>
11:30 am – 01:15 pm	<b>Session 5 Cont...</b>
01:15 pm – 02:15 pm	<b>Lunch Break</b>
02:15 pm – 03:30 pm	<b>Practice of session 5</b>
03:30 pm– 04:00 pm	<b>Doubts and Discussions</b>

**DAY:06**

10:00 am – 11:15 am	<b>Session 6.</b> - Hands on training on 7 segment display - RTC,EEPROM
11:15 am – 11:30 am	<b>Coffee Break</b>
11:30 am – 01:15 pm	<b>Session 6 Cont...</b>
01:15 pm – 02:15 pm	<b>Lunch Break</b>
02:15 pm – 03:30 pm	<b>Practice of session 6</b>
03:30 pm– 04:00 pm	<b>Doubts and Discussions</b>

**DAY:07**


10:00 am – 11:15 am	<b>Session 7.</b> - Hands on training on VHDL Programming. - Overview
11:15 am – 11:30 am	<b>Coffee Break</b>
11:30 am – 01:15 pm	<b>Session 7 Cont...</b>
01:15 pm – 02:15 pm	<b>Lunch Break</b>
02:15 pm – 03:30 pm	<b>Practice of session 7</b>
03:30 pm– 04:00 pm	<b>Doubts and Discussions</b>

**DAY:08**

10:00 am – 01:15 pm	<b>Session 8.</b> - Hands on training on hardware interface - On serial port - ADC
11:15 am – 11:30 am	<b>Coffee Break</b>
11:30 am – 01:15 pm	<b>Session 8 Cont...</b>
01:15 pm – 02:15 pm	<b>Lunch Break</b>
02:15 pm – 03:30 pm	<b>Practice of session 8</b>
03:30 pm– 04:00 pm	<b>Doubts and Discussions</b>

**DAY:09**

10:00 am – 01:15 pm	<b>Session 9.</b> - DC motor interface - Program with PWM
11:15 am – 11:30 am	<b>Coffee Break</b>
11:30 am – 01:15 pm	<b>Session 9 Cont...</b>
01:15 pm – 02:15 pm	<b>Lunch Break</b>
02:15 pm – 03:30 pm	<b>Practice of session 9</b>
03:30 pm– 04:00 pm	<b>Doubts and Discussions</b>

  
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**DAY:10**

10:00 am – 01:15 pm	<b>Session 10.</b> - Introduction to XLINIX ISE IDE. - overview
11:15 am – 11:30 am	<b>Coffee Break</b>
11:30 am – 01:15 pm	<b>Session 10 Cont...</b>
01:15 pm – 02:15 pm	<b>Lunch Break</b>
02:15 pm – 03:30 pm	<b>Practice of session 10</b>
03:30 pm– 04:00 pm	<b>Doubts and Discussions</b>

**DAY:11**

10:00 am – 01:15 pm	<b>Session 11.</b> - Accessing on-chip ADC and to display the value in LCD temperature sensors
11:15 am – 11:30 am	<b>Coffee Break</b>
11:30 am – 01:15 pm	<b>Session 11 Cont...</b>
01:15 pm – 02:15 pm	<b>Lunch Break</b>
02:15 pm – 03:30 pm	<b>Practice of session 11</b>
03:30 pm– 04:00 pm	<b>Doubts and Discussions</b>

**DAY:12**

10:00 am – 01:15 pm	<b>Session 12.</b> - Accessing on-chip ADC to display the value in PC serial terminal temperature sensors.
11:15 am – 11:30 am	<b>Coffee Break</b>
11:30 am – 01:15 pm	<b>Session 12 Cont...</b>
01:15 pm – 02:15 pm	<b>Lunch Break</b>
02:15 pm – 03:30 pm	<b>Practice of session 12</b>
03:30 pm– 04:00 pm	<b>Doubts and Discussions</b>
04:00 pm – 04:10 pm	<b>validity and photo session</b>

  
**Co-ordinator**

  
**HoD**  
**HEAD OF THE DEPARTMENT**  
**Electronics & Communication Engineering**  
**PALLAVI ENGINEERING COLLEGE**  
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**Ranga Reddy Dist-501 505**

  
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Abdullapurmet(M), Near Hayathanagar,Hyderabad.

## DEPARTMENT OF ECE

### SUMMARY REPORT OF THE PROGRAM

S.No.	Name of the Course	Duration	No.of Students Registered	No.of Students Benefited
1	FPGA	12days (60 hours)	66	62

  
Co-ordinator

  
HoD  
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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

### OUT COME OF THE PROGRAM

#### One Week Certificate Program On FPGA Programming

- Understand the concepts of FPGA Programming and Architecture of FPGA
- Identify the Application and Purpose of FPGA Programming, UART programming & Design Process Requirements
- Writing of Commands used in programming by using XLINIX ISE IDE tool
- Hands on training of different modules by VHDL programming on tool of XLINIX ISE IDE

HOD

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## Activity Report on Two week Certificate Program on FPGA Programming

Name of the Activity	Two week Certificate Program on FPGA Programming	
Type of Activity	Two week Certificate Program for students	
Date and Time of Activity	07/12/2020 to 19/12/2020	10.00 am to 4.00 pm
Details of Participants	62 Students participated	
Coordinator(s)	Mr. B Narendar Reddy, Asst. Prof.	Mr. P Nanda Kumar, Asst. Prof.
Organizing Dept./Support System	ECE	
In collaboration with	Ms. Shalini Bhattacharya, Kongu Engineering college, Perunduri, Dr. Senthil Kumar, Asoc. Prof. Ashoka Institute of Engineering & Technology, Hyd.	
Description	<p>This Program started with an inaugural function of welcoming chief guests. After addressing the gathering by eminent persons, the session started with introduction to FPGA programming and VHDL programming. In the afternoon it was complete hands on session on the topics discussed in the morning session. This Program included with contents like introduction to XLINIX, interfacing various elements with VHDL, LED and LCD interfacing. All the interfacing sessions were trained in hands on session.</p> <p>During this period students were able to understand what FPGA is? how it is related to the different elements, how far is the programming better when compared to other programming languages. Students were able to interface the DC motor using FPGA programming.</p> <p>Valedictory session was held after successful completion of the Program. Feedback was taken from students in both oral and written form.</p>	

  
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Participants (62)

Search

Narendar Reddy  
(host, me)

Guest

17-411 HRITHIK

17-420 SRIKAN...

176F1A0407

176F1A0408 M...

176F1A0415 Ji...

176F1A0416 s...

176F1A0419

176F1A0422 P...

176F1A0424 \_P...

176F1A0428 R...

176F1A0435

176F5A0403 s...

176F5A0422 u...

186F5A0402 A...

186F5A0406

186F5A0411

186F5A0421je...

186F5A0427

186F5A0429 vi...

18C25A0402

anusha\_18-405

196F1A0418 preethi

196F1A0419 sandhya

196F1A0421 jesmitha

196F1A0422

196F1A0425M Naveena

196F1A0427 Meghana

196f1A0429 TARAKPRA...

196F1A0431-jyothsna

196F1A0432 sairam

Invite Chat Mute Control More

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Photo

Media coverage (if any)	No
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Co-ordinator

  
**HEAD OF THE DEPARTMENT**  
**Electronics & Communication Engineering**  
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## PALLAVI ENGINEERING COLLEGE

Kuntloor (V), Hayathnagar (M), R.R. Dist:- 501 505, Greater Hyderabad

Two Week Certificate Program On FPGA Programming

STUDENTS ATTENDANCE SHEET

From 07-12-2020 to 19-12-2020

S.N	Hall ticket no.	Name of the student	Day1	Day2	Day3	Day4	Day5	Day6	Day7	Day8	Day9	Day10	Day11	Day12
1	196F1A0401	A PAVANI	✓	✓	✓	✓	✓	X	✓	✓	✓	✓	✓	✓
2	196F1A0403	AMBATI SRUJANA	✓	X	✓	✓	✓	✓	✓	X	✓	✓	✓	✓
3	196F1A0404	ANNAM SRIHARI	✓	✓	✓	X	✓	✓	✓	✓	✓	✓	✓	✓
4	196F1A0408	BIGNESH KUMAR	✓	✓	✓	✓	✓	✓	X	✓	✓	✓	✓	✓
5	196F1A0412	CH SOWMYA	✓	✓	X	✓	✓	✓	✓	✓	✓	✓	✓	✓
6	196F1A0413	CHIPPALA ASHWINI	✓	✓	✓	✓	X	✓	✓	✓	✓	✓	✓	✓
7	196F1A0414	CH PADMA PRIYA	✓	✓	✓	X	✓	✓	✓	✓	✓	✓	✓	✓
8	196F1A0415	D VISHNU VARDHAN	✓	✓	✓	✓	X	✓	✓	✓	✓	✓	✓	✓
9	196F1A0420	K V SAI SURESH	✓	✓	✓	✓	X	✓	✓	✓	✓	✓	✓	✓
10	196F1A0421	KAVURI JESMITHA	✓	✓	X	✓	✓	✓	✓	✓	✓	✓	✓	✓
11	196F1A0427	P MEGHANA	✓	✓	✓	✓	✓	✓	X	✓	✓	X	✓	✓
12	196F1A0431	R JYOTHSNA	X	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
13	196F1A0432	SAMPETA SAIRAM	✓	✓	✓	X	✓	✓	✓	✓	✓	✓	✓	X
14	196F1A0434	S VYBHAVI RAO	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
15	196F1A0437	BHARATH CHANDRA	Ab	Ab	Ab	Ab	Ab	Ab	Ab	Ab	Ab	Ab	Ab	Ab
16	196F1A0438	ARAVINDSWAMY	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
17	196F1A0439	VENDRA JYOTHI	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
18	196F1A0440	AMBATI SUSHMA	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
19	206F5A0401	D SANTHOSH KUMAR	✓	✓	X	✓	✓	✓	✓	✓	✓	✓	✓	✓
20	206F5A0402	G SRUTHI	✓	✓	✓	✓	X	✓	✓	✓	✓	✓	✓	✓
21	206F5A0403	K ADITYA YASHWANTH	✓	X	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
22	206F5A0404	K UMA	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
23	206F5A0405	MD ZUBAIR	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
24	206F5A0406	P RAKESH	Ab	Ab	Ab	Ab	Ab	Ab	Ab	Ab	Ab	Ab	Ab	Ab
25	206F5A0407	R SHIVANI CHARY	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
26	206F5A0408	R SHASHI KUMAR	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
27	186F1A0401	A.ANJI	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	X	✓

  
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63	186F5A0419	T PRANEETH GOUD	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
64	186F5A0421	V JEEVAN KUMAR	✓	✗	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
65	186F5A0427	NARAMULA JHANSI	✓	✓	✓	✓	✓	✓	✗	✓	✓	✓	✓	✓
66	186F5A0429	S VINEETHA	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓

  
Co-ordinator

  
HoD

  
Principal  
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## 'FPGA PROGRAMMING'

### Two Week Certificate Program Evaluation Form

Please return this form to the instructor or organizer at the end of the workshop. Thank you.

Program title: FPGA Programming  
 Date: 19-12-2020 Instructor: Dr. Senthil Kumar

	Strongly agree				Strongly disagree
1. The content was as described in publicity materials	1	2	3	4	5
2. This Program was applicable to my job	1	2	3	4	5
3. I will recommend this Program to other conservators	1	2	3	4	5
4. The program was well paced within the allotted time	1	2	3	4	5
5. The instructor was a good communicator	1	2	3	4	5
6. The material was presented in an organized manner	1	2	3	4	5
7. The instructor was knowledgeable on the topic	1	2	3	4	5
8. I would be interested in attending a follow-up, more advanced Program on this same subject	1	2	3	4	5

9. Given the topic, was this Program:       a. Too short     b. Right length     c. Too long

10. In your opinion, was this Program:       a. Introductory     b. Intermediate     c. Advanced

11. Please rate the following:

	Excellent	Very Good	Good	Fair	Poor
a. Visuals	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
b. Acoustics	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
c. Meeting space	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
d. Handouts	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
e. The program overall	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

12. What did you most appreciate/enjoy/think was best about the course? Any suggestions for improvement?

good.

---




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Thank you!

Please return this form to the instructor or coordinator at the end of the Program.

  
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## 'FPGA PROGRAMMING' Two Week Certificate Program Evaluation Form

Please return this form to the instructor or organizer at the end of the workshop. Thank you.

Program title: FPGA Programming  
 Date: 19/12/20 Instructor: Dr. Senthil Kumar

	Strongly agree	2	3	4	Strongly disagree
1. The content was as described in publicity materials	1	2	3	4	5
2. This Program was applicable to my job	1	2	3	4	5
3. I will recommend this Program to other conservators	1	2	3	4	5
4. The program was well paced within the allotted time	1	2	3	4	5
5. The instructor was a good communicator	1	2	3	4	5
6. The material was presented in an organized manner	1	2	3	4	5
7. The instructor was knowledgeable on the topic	1	2	3	4	5
8. I would be interested in attending a follow-up, more advanced Program on this same subject	1	2	3	4	5
9. Given the topic, was this Program:	<input type="checkbox"/> a. Too short <input checked="" type="checkbox"/> b. Right length <input type="checkbox"/> c. Too long				
10. In your opinion, was this Program:	<input type="checkbox"/> a. Introductory <input checked="" type="checkbox"/> b. Intermediate <input type="checkbox"/> c. Advanced				
11. Please rate the following:					
	Excellent	Very Good	Good	Fair	Poor
a. Visuals	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
b. Acoustics	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
c. Meeting space	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
d. Handouts	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
e. The program overall	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

12. What did you most appreciate/enjoy/think was best about the course? Any suggestions for improvement?

Good.

---




---



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Thank you!

Please return this form to the instructor or coordinator at the end of the Program.

  
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## CERTIFICATE


### OF PARTICIPATION

This certificate accredited that


Mr./ Ms./ Mrs. Bignesh Kumar has successfully


Completed two week Certificate Program on **"FPGA Programming"** organized by the Department of Electronics and Communication Engineering held at Pallavi Engineering College, Hyderabad.

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This certificate accredited that

Mr./ Ms./ Mrs. A. Pavani has successfully

Completed two week Certificate Program on "**FPGA Programming**" organized by the Department of Electronics and Communication Engineering held at Pallavi Engineering College, Hyderabad.

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Mr./ Ms./ Mrs. Ambati. Soujanya has successfully


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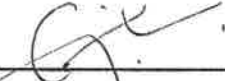
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Mr./ Ms./ Mrs. A. Sathari \_\_\_\_\_ has successfully

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Mr./ Ms./ Mrs. CH. Sowmya ----- has successfully

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Mr./ Ms./ Mrs. CH. Ashwini ----- has successfully

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Mr./ Ms./ Mrs. D. Vishnu Vardhan ----- has successfully

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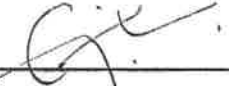
Mr./ Ms./ Mrs. Kavuri Jesmita has successfully


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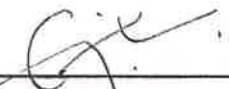
Mr./ Ms./ Mrs. P. Meghana has successfully

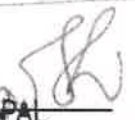
Completed two week Certificate Program on **"FPGA Programming"** organized by the Department of Electronics and Communication Engineering held at Pallavi Engineering College, Hyderabad.

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Mr./ Ms./ Mrs. R. Jyotsna has successfully


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Mr./ Ms./ Mrs. Sampeta. Sairam has successfully

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Mr./ Ms./ Mrs. S. Vybhavi Rao has successfully


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OF PARTICIPATION

This certificate accredited that


Mr./ Ms./ Mrs. Asavind Swamy has successfully


Completed two week Certificate Program on **"FPGA Programming"** organized by the Department of Electronics and Communication Engineering held at Pallavi Engineering College, Hyderabad.

During **07.12.2020 to 19.12.2020**

  
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Mr./ Ms./ Mrs. V. Jyotsi has successfully

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Mr./ Ms./ Mrs. A. Sushma has successfully

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Mr./ Ms./ Mrs. D. Santosh Kumar has successfully


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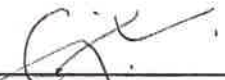
Mr./ Ms./ Mrs. K. Aditya Yashwanth has successfully


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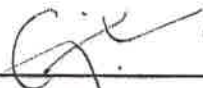
Mr./ Ms./ Mrs. R. Shivani Chary has successfully


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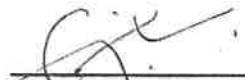
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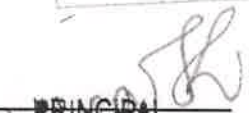
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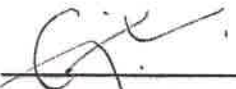
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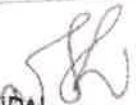
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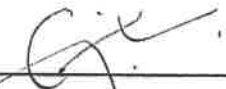
Mr./ Ms./ Mrs. G. Shiva Kumar has successfully

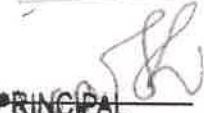
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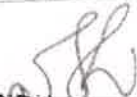
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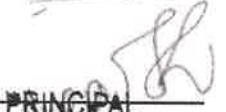
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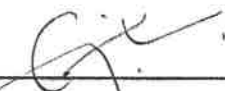
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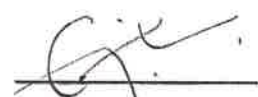
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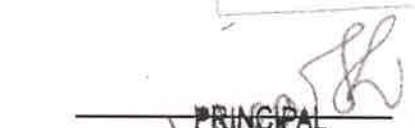
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